

FIG. 1 is a block diagram of a video camera system. The system includes a CCD 15, an analog signal processor 20, an A/D converter 25, a DSP 30, an encoder 55, a frame memory 35, a memory controller 45, a micro processor 40, a timing signal generator 60, a driving signal generator 65, and a compression memory 50. The system is connected to a TV, LCD. A switch S1 is connected to the micro processor 40 and ground.

FIG. 1

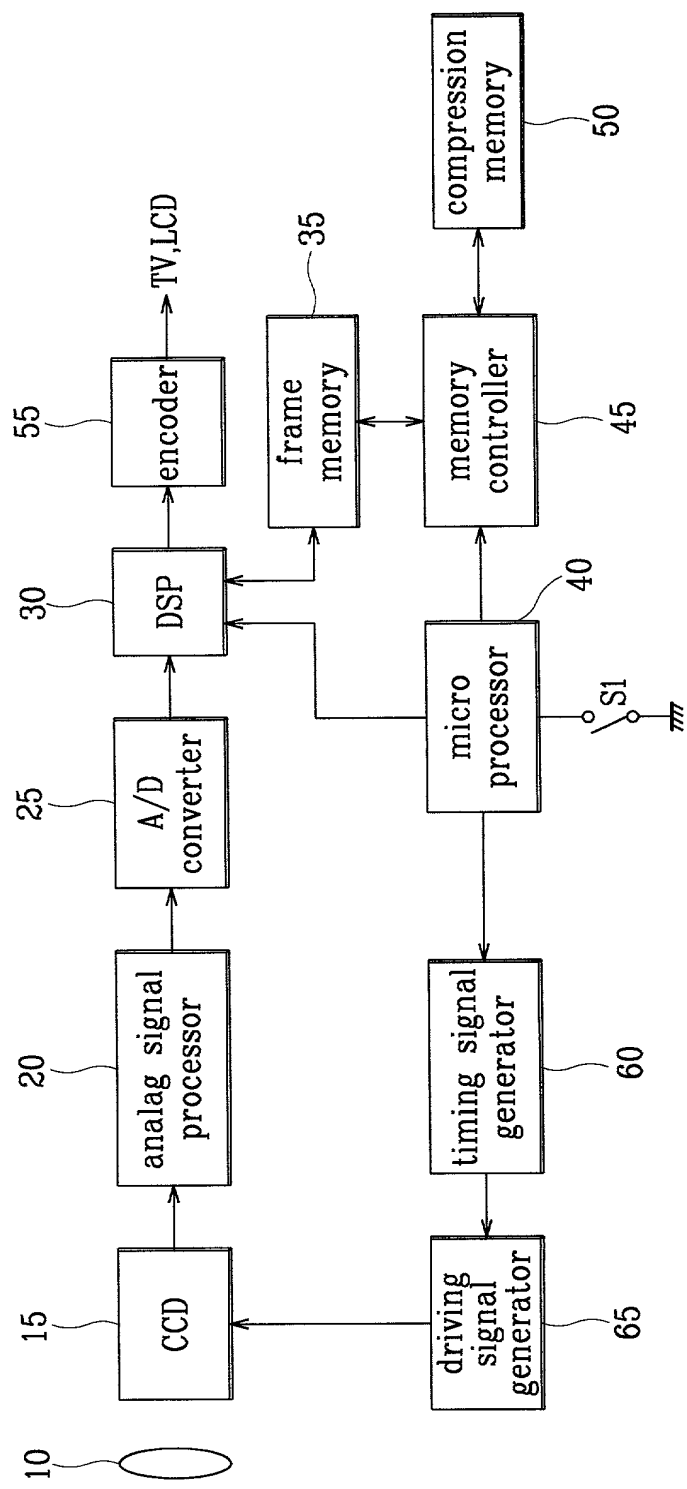


FIG.2

